

A block diagram of a dual-chip system. Two rectangular blocks, each labeled "MMIC Chip", are positioned side-by-side within a larger rectangular frame. Each chip has four small square pads at its corners. To the left of the first chip is a U-shaped component labeled 22, with an arrow pointing towards the chip. To the right of the second chip is a similar U-shaped component labeled 30, with an arrow pointing towards the chip. A horizontal line connects the two chips. Various reference numerals are used: 20 points to the top pads of both chips; 26 points to the bottom pads of both chips; 28 points to the left and right pads of the first chip, and the right pad of the second chip; 24 points to the top-right corner of the frame; 31 points to the right pad of the second chip.

FIG. 2 (PRIOR ART)

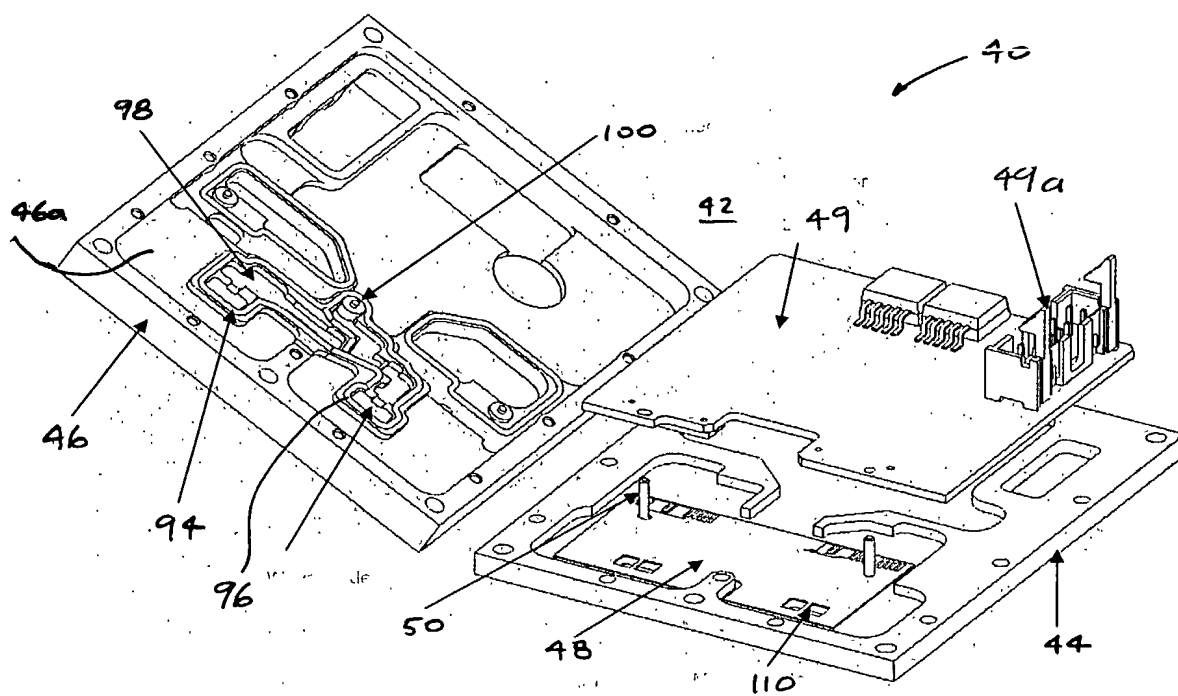


FIG. 3

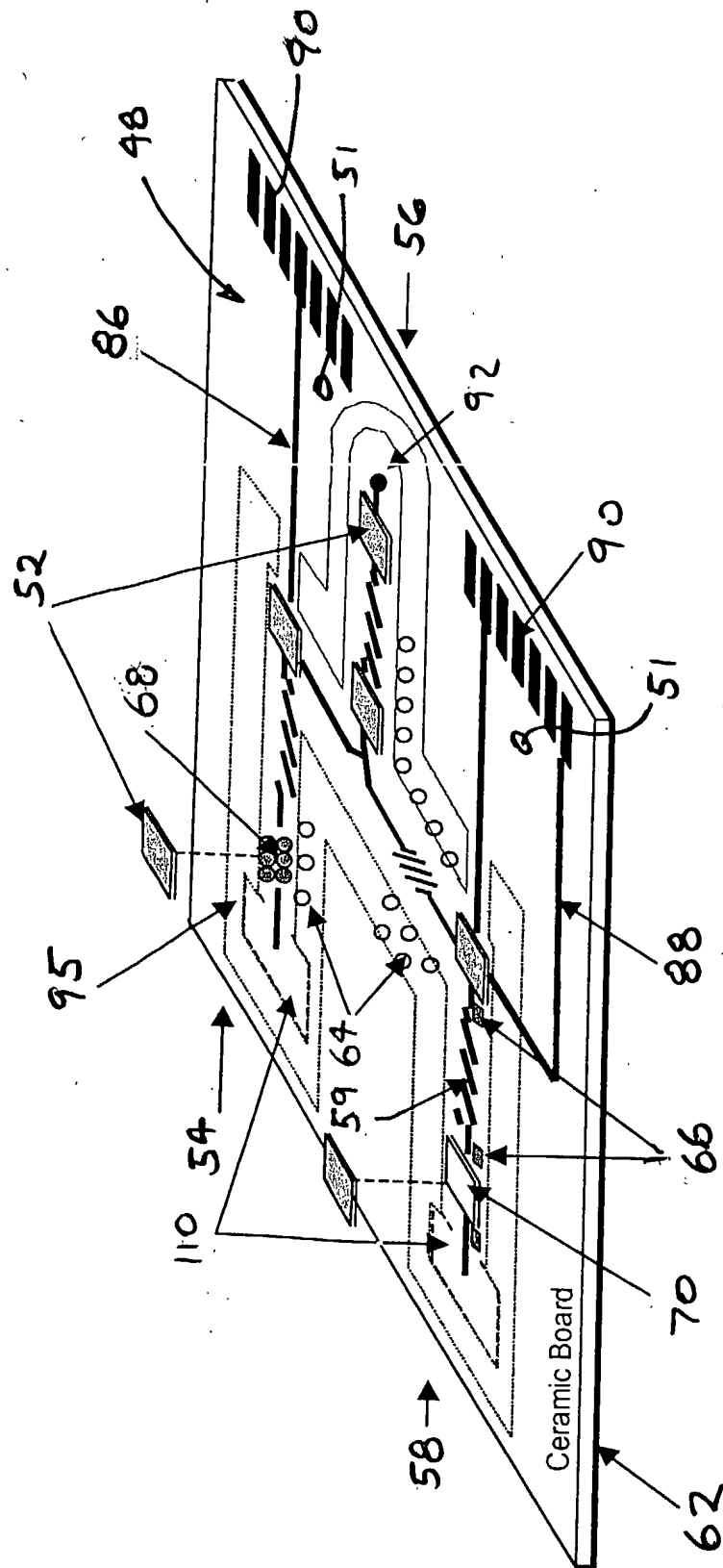
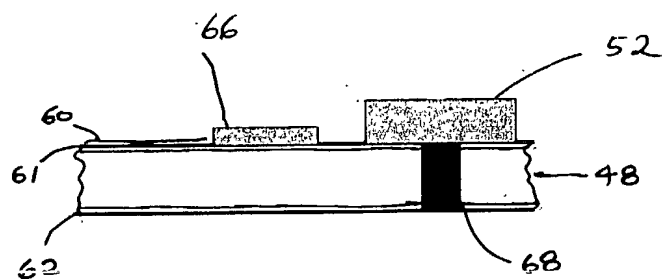
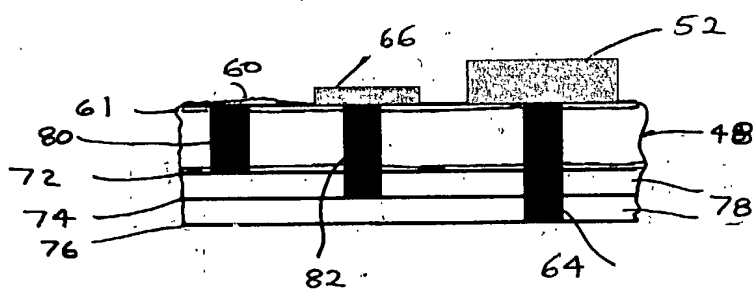


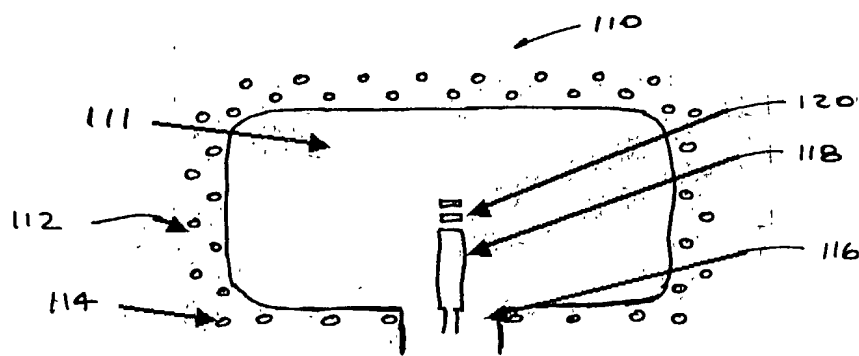
FIG. 4



F.G.5



F.G.6



F.G.7

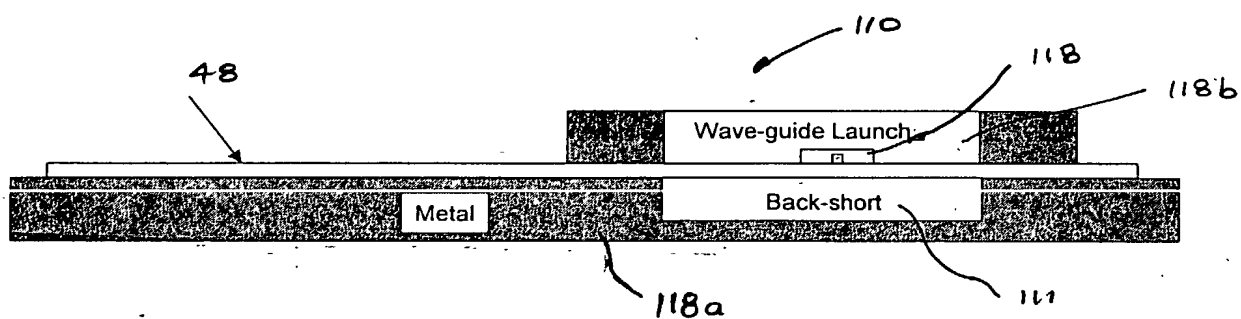
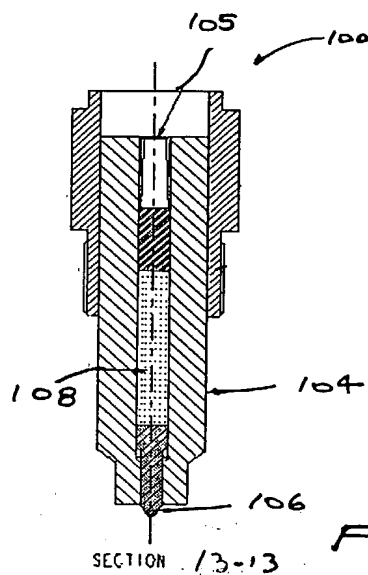
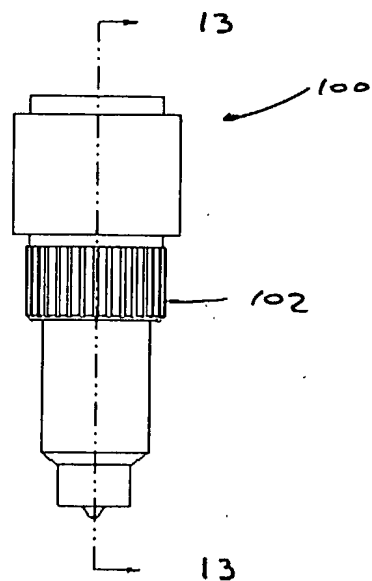
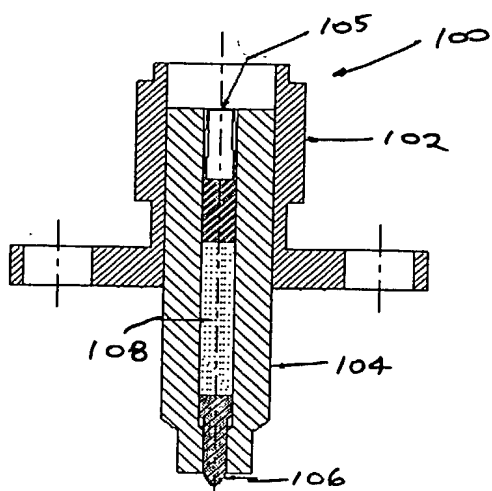
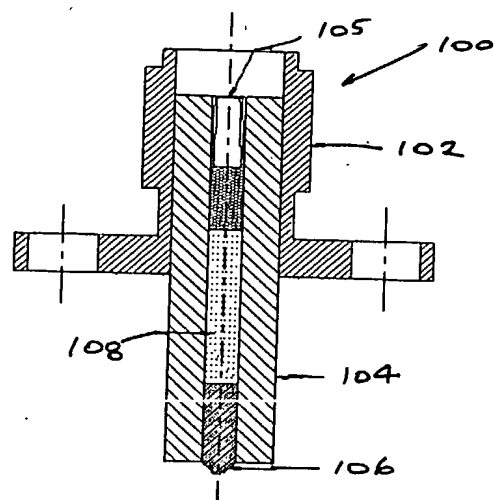
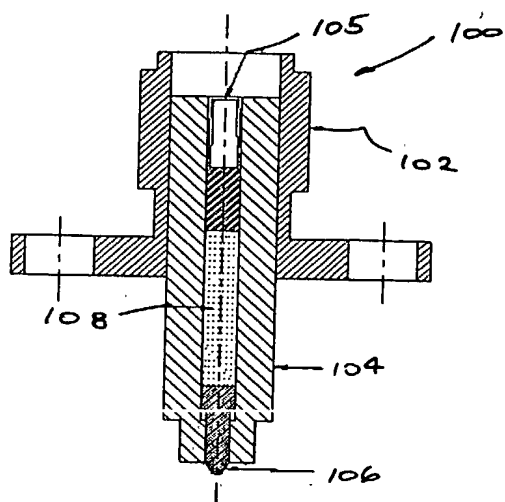


FIG. 8



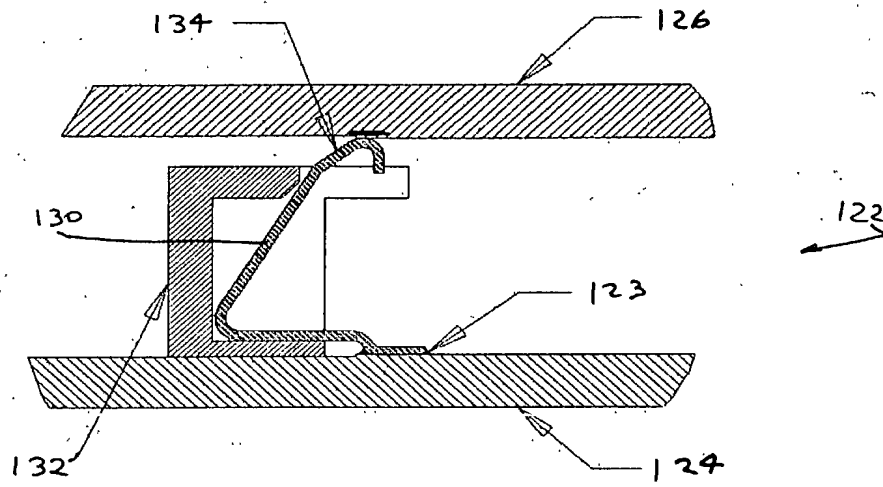


FIG. 14

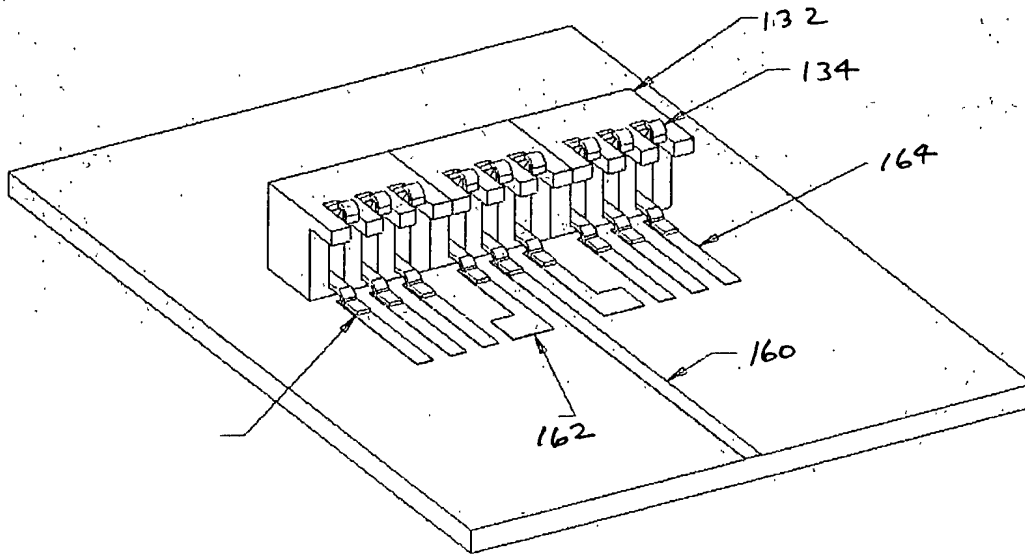


FIG. 15